

Fig. 1

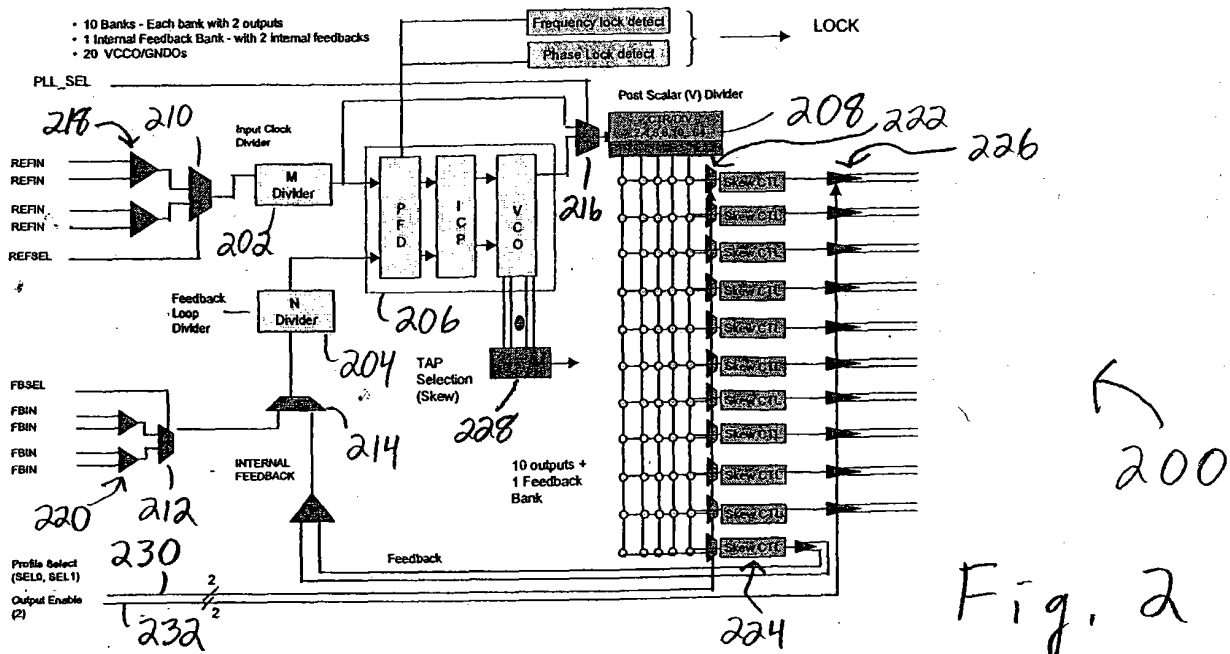


Fig. 2

# External Frequency Select Control & Five Different Frequency Generations

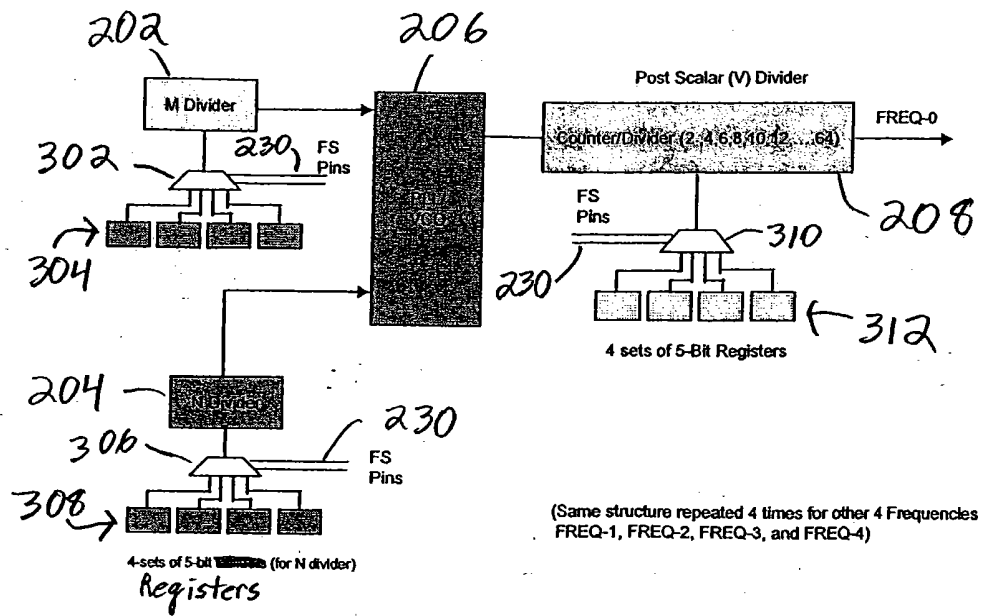


Fig. 3

# External Frequency Select Control & Five Different Frequency Generations

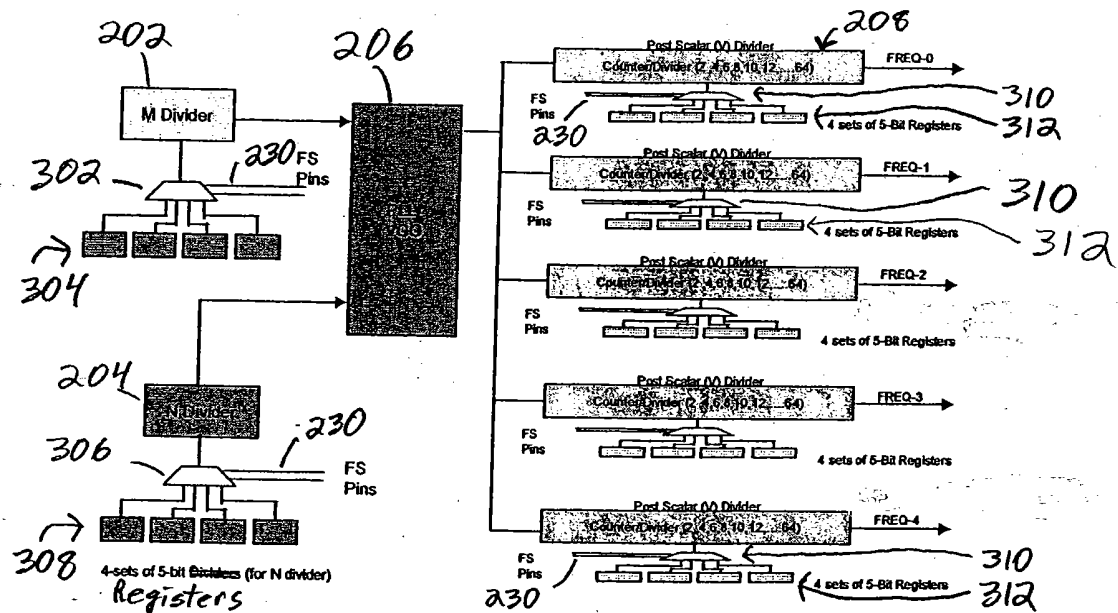


Fig. 4

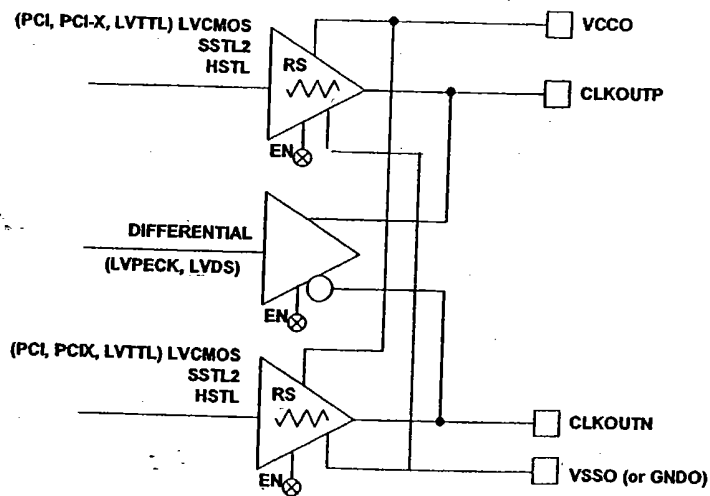


Fig. 5

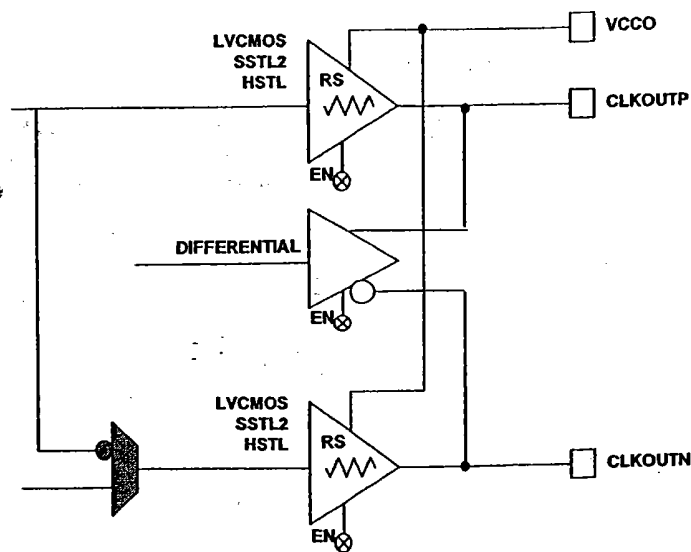


Fig. 6

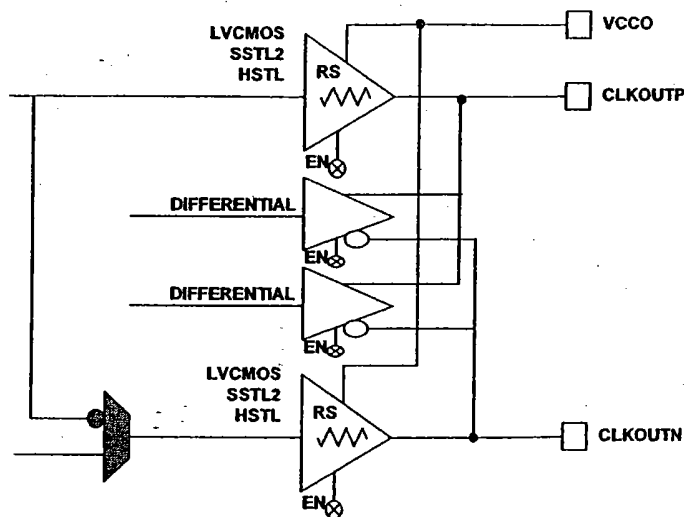
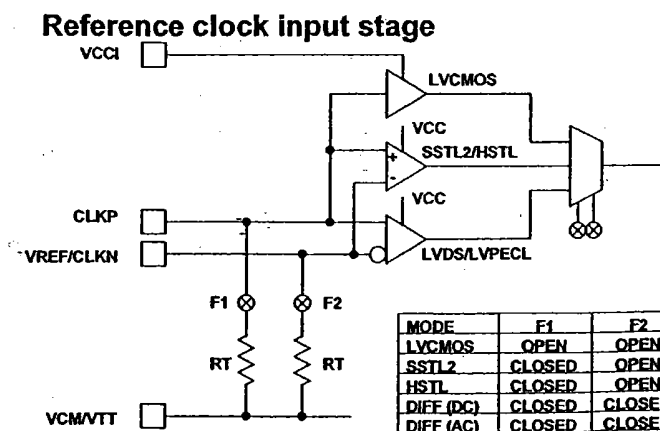


Fig. 7



800

Fig. 8

## SSTL2/HSTL input application

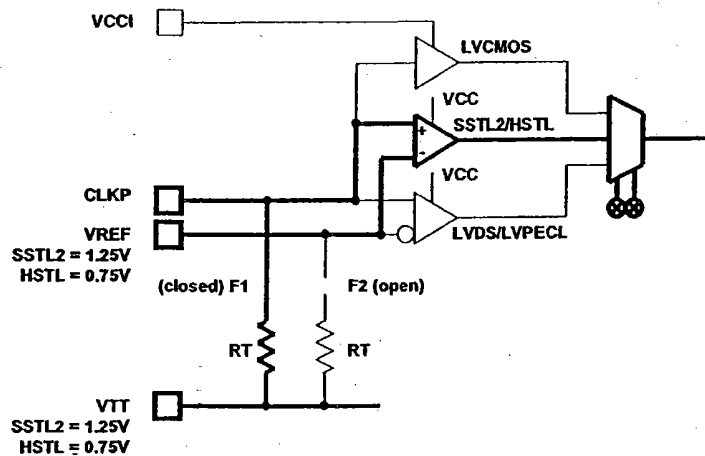


Fig. 9

## Alternative DC coupled differential application

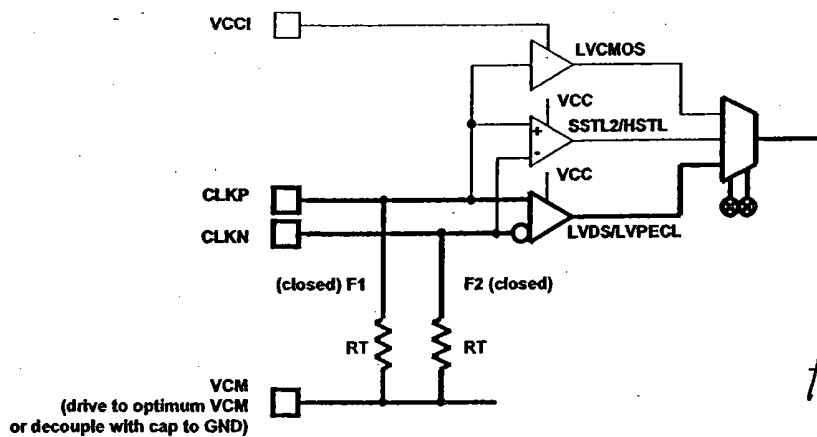
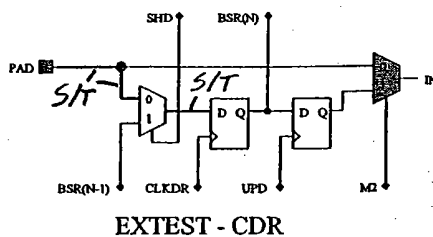


Fig. 10



Input Boundary Scan Cell

Fig. 11

1100

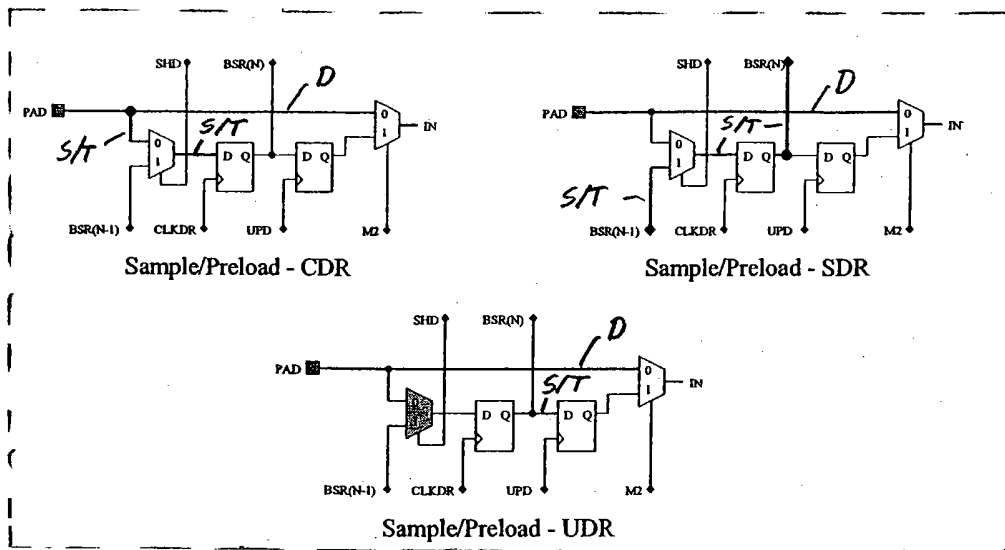


Fig. 12 - Sample/Preload BSC Operation

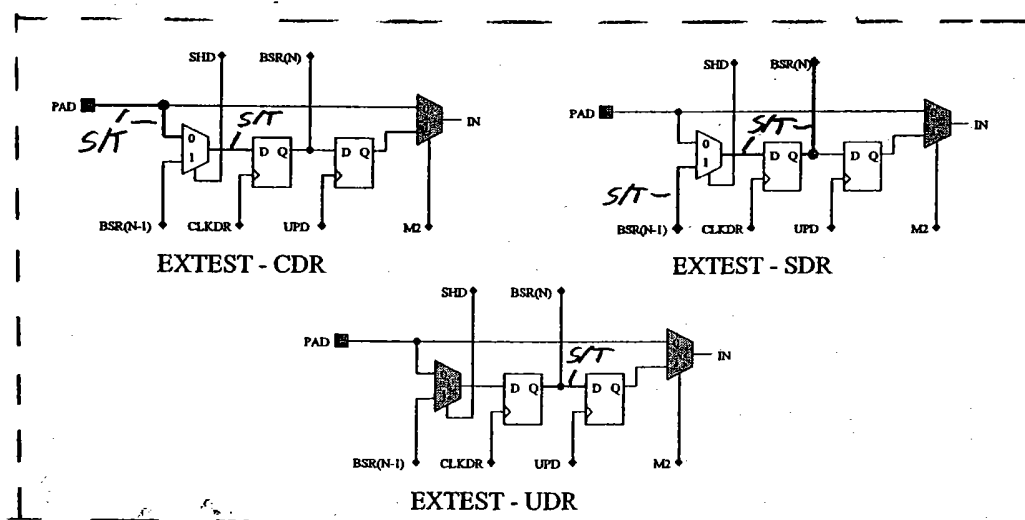


Fig. 13 - EXTEST BSC Operation

